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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/654,943	09/01/2000	Seung Kuk Ahn	8733-294-00	7898
30827	7590 02/03/2004		EXA	MINER
	LONG & ALDRIDG	KOVALICK, VINCENT E		
1900 K STREET, NW WASHINGTON, DC 20006			ART UNIT	PAPER NUMBER
	,		2673	5
			DATE MAILED: 02/03/2004	4

Please find below and/or attached an Office communication concerning this application or proceeding.

		I A	pplication No.	Applicant(s)			
Office Action Summary			9/654,943	AHN, SEUNG KUI	K		
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Period fo		почания прочения		. War are concepting and			
THE - Exte after - If the - If NO - Failtu - Any	ORTENED STATUTORY PERIOD IS MAILING DATE OF THIS COMMUN nsions of time may be available under the provision SIX (6) MONTHS from the mailing date of this come period for reply specified above, the maximum street or reply within the set or extended period for reply received by the Office later than three months end patent term adjustment. See 37 CFR 1.704(b).	IICATION. s of 37 CFR 1.136(a munication. 30) days, a reply with statutory period will a y will, by statute, cau). In no event, however, ma hin the statutory minimum of pply and will expire SIX (6) I use the application to becom	y a reply be timely filed thirty (30) days will be considered timely MONTHS from the mailing date of this co e ABANDONED (35 U.S.C. § 133).			
1)⊠	Responsive to communication(s) fil	ed on <u>17 Nove</u>	ember 2003.				
,		 2b)⊠ This act	ion is non-final.				
3)	,—						
Disposit	ion of Claims						
4)🖂	☑ Claim(s) 1-20 is/are pending in the application.						
	4a) Of the above claim(s) is/are withdrawn from consideration.						
5)	Claim(s) is/are allowed.						
6)⊠	☑ Claim(s) <u>1-6 and 10-17</u> is/are rejected.						
7)⊠	☑ Claim(s) <u>7-9 and 18-20</u> is/are objected to.						
8)[Claim(s) are subject to restri	ction and/or el	ection requirement.				
Applicat	ion Papers						
9)[The specification is objected to by the	ne Examiner.					
10)	The drawing(s) filed on is/are	e: a) 🗌 accept	ed or b)⊡ objected	to by the Examiner.			
	Applicant may not request that any obje	ection to the dra	wing(s) be held in abe	yance. See 37 CFR 1.85(a).			
	Replacement drawing sheet(s) including	g the correction	is required if the draw	ing(s) is objected to. See 37 CF	R 1.121(d).		
11)	The oath or declaration is objected to	to by the Exam	iner. Note the attac	hed Office Action or form PT	O-152.		
Priority (under 35 U.S.C. §§ 119 and 120						
a) 13)	Acknowledgment is made of a claim All b) Some * c) None of: 1. Certified copies of the priority 2. Certified copies of the priority 3. Copies of the certified copies application from the Internation of the attached detailed Office action of the specific reference was included to the translation of the foreign to the complete the translation of the foreign to the complete the translation of the first services.	documents had documents had documents had documents had been done on a list of the for domestic period of the first subject of the first subject had domestic period domestic	ave been received. ave been received in documents have been to the contents have been to the certified copies of the specific properties of the specific properties application has to the certific properties of the specific properties application of the specific properties are the certification of the specific properties are the certification of the certific properties are the certification of the c	n Application No en received in this National S not received. C. § 119(e) (to a provisional ification or in an Application I s been received. C. §§ 120 and/or 121 since a	application) Data Sheet. a specific		
Attachmen	et(s) of References Cited (PTO-892)		4) 🗍 Intende	ew Summary (PTO-413) Paper No(s			
2) Notic	æ of References Cited (FTO-692) æ of Draftsperson's Patent Drawing Review (mation Disclosure Statement(s) (PTO-1449) I			of Informal Patent Application (PTO			

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DETAILED ACTION

Response to Amendment

1. This Office Action is in response to Applicant's Amendment dated November 17, 2003 in response to USPTO Office Action dated May 16, 2003.

Authorization to correct the minor informalities in Figures 5A, 5B, 7A, 7B, 8A and 8B as submitted is granted.

The addition of new claims 10-20 is noted and said claims have been entered in the record.

Regarding Applicant's remarks relating to the objection to claims 1 an 5 based on the lack of an antecedent basis for the claimed subject matter, though sufficient to overcome the objection to the specification by pointing out the multiple pages identified to support the said claims, said specification support clearly fall short of a clear and concise antecedent.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 3. Claims 1-5, 11 and 15-16 are rejected under 35 U.S.C. 102(e) as being anticipated by Hirakata (USP 6,496,172).

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Relative to claims 1, 5, 11 and 16, Hirakata teaches a liquid crystal display device, active matrix type liquid crystal display device, and method of driving the same (col. 5, lines 41-67; col. 6, lines 1-67 and col. 7, lines 1-16); Hirakata further teaches an apparatus and method for driving a liquid crystal panel having pixels arranged at each intersection between gate lines and data lines in a matrix type in an inversion system, comprising: first signal supplying means for setting at least one pixel block each of which includes at least two data lines within the liquid crystal panel to apply data signals having the same polarity to the adjacent pixels in a gate line direction within the pixel block; and second signal supplying means for applying data signals having a polarity contrary to the adjacent pixels at the left and right sides thereof to the pixels within the other pixel areas except for the pixel block area (col. 8, lines 36-54 and Figs. 17A and 17B). Regarding claim 2, Hirakata teaches driving a liquid crystal panel wherein the pixel block is positioned at a boundary portion between column drivers (col. 16, lines 54-59) At to claim 3, Hirakata teaches driving a liquid crystal panel wherein the pixel block includes at least two data lines to which a data is applied form the same column diver (col. 16, lines 45-53). Relative to claims 4 and 15, Hirakata teaches driving a liquid crystal panel wherein all the pixels within the liquid crystal panel responds to the data signals having a polarity inverted every frame (col. 3, lines 22-25).

Claim Rejections - 35 USC § 103

- 4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

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5. Claims 6 and 17 are rejected under 35 U.S.C. 103(a) as being unpatentable over Hirakata as applied to claims 5 and 16 respectively in item 3 hereinabove, and further in view of Morita (USP 6,628,274).

Regarding claims 6 ands 17, Hirakata **does not teach** a liquid crystal panel comprising line-inversion control means for controlling the first signal supplying means to apply the data signals having the same polarity to the adjacent pixels in the gate line direction; and dot-inversion control means for controlling the second signal supplying means to apply the data signals having a polarity contrary to the pixels at the left and right sides thereof.

Hirakata teaches a liquid crystal display device with no flicker and with a bright display by using a frame inversion driving technique.

Morita teaches a display driving device for driving liquid crystal by applying a voltage to the liquid crystal based on gray-scale data representative of a display; Morita further teaches a liquid crystal panel comprising line-inversion control means for controlling the first signal supplying means to apply the data signals having the same polarity to the adjacent pixels in the gate line direction; and dot-inversion control means for controlling the second signal supplying means to apply the data signals having a polarity contrary to the pixels at the left and right sides thereof (col. 7, lines 28-49).

It would have been obvious to a person of ordinary skill in the art at the time of the invention to provide to the device as taught by Hirakata the feature as taught by Morita in order to optimize the drive device output.

6. Claims 10, 12, 13 and 14 are rejected under 35 U.S.C. 103(a) as being unpatentable over Hirakata as applied to claims 2 and 11 respectively in item 3 hereinabove, and further in view of

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Jeong et al. (USP 6,271,816).

Regarding claims 10 and 13, Hirakata does not teach a method wherein data lines within a least one first plurality of consecutively arranged data lines are connected to adjacent column drivers. Hirakata teaches a liquid crystal display device with no flicker and with a bright display by using a frame inversion driving technique.

Jeong teaches a power saving circuit and method for driving an active matrix display (col. 3, lines 62-67 and col. 4, lines 1-7); Jeong et al. further teaches teach a method wherein data lines within a least one first plurality of consecutively arranged data lines are connected to adjacent column drivers (col. 2, lines 60-67; col. 5, lines 10-23 and col. 6, lines 49-62).

It would have been obvious to a person of ordinary skill in the art at the time of the invention to provide to the device as taught by Hirakata the feature as taught by Jeong et al. in order to provide the means to significantly reduce the power needed by the column drive circuit to drive voltages of alternating polarity onto the column electrodes, in this way significant power is saved in both the pixel inversion and the row inversion schemes (Jeong et al. col. 3, lines 66-67 and col. 4, lines 1-3).

Relative to claims 12 and 14, Jeong et al. further **teaches** the method step of providing a plurality of column drivers for applying the video signal, wherein each column driver is connected to a plurality of consecutively arranged data lines (col. 4, lines 65-67; col. 5, lines 1-9 and Fig. 1A).

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Allowable Subject Matter

- 7. Claims 7-9 and 18-20 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
- 8. Regarding claims 7 and 18, the major difference between the teachings of the prior art of record (USP 6,496,172, Hirakata; USP 6,628,274, Morita and USP 6,271,816, Jeong et al.) and that of the instant invention is that said prior art of record **does not teach** an apparatus for driving a liquid crystal panel wherein the first and second signal supplying means comprises at least two signal inverters for responding to control signals applied from the line inversion control means and the dot-inversion control means to invert phases of input data signals.

Conclusion

9. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

U. S. Patent No.	6,559,822	Okuzono
U. S. Patent No.	6,400,350	Nishimura et al.
U. S. Patent No.	6,327,008	Fujiyoshi

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Responses

10. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Vincent E Kovalick whose telephone number is 703 306-3020. The examiner can normally be reached on Monday-Thursday 7:30- 4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin Shalwala can be reached on 703 305-4938. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703 306-0377.

Vincent E. Kovalick January 28,2004

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